



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/769,573
Filing Date January 30, 2004
Inventorship Mark A. Helm
Assignee Micron Technology, Inc.
Group Art Unit 2826
Examiner Fetsum Abraham
Attorney's Docket No. MI22-2478
Customer No. 021567
Title: Gated Semiconductor Assemblies and Methods of Forming Gate
Semiconductor Assemblies

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT


References - See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

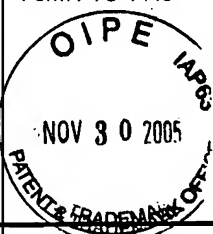
This Supplemental Information Disclosure Statement is being filed together with a Request for Continued Examination application. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 30 Nov 2005

By: 
James E. Lake
Reg. No. 44,854

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Form PTO-1449 		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-2478		SERIAL NO. 10/769,573	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT: Mark A. Helm			
U.S. PATENT DOCUMENTS				FILING DATE January 30, 2004		GROUP 2826	
*Examiner's Initials	AA	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AB						
	AC						
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FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
	AJ	JP409055351A	02/1997	Japan (and Abstract)			X
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
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